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Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

**Complete if Known**

Application Number	10743,307
Filing Date	December 23, 2003
First Named Inventor	ZHANG, Qi
Art Unit	2183 2193
Examiner Name	Not yet known <i>Chen, J.</i>
Attorney Docket Number	P-6215-US

Sheet	1	of	2	Attorney Docket Number	P-6215-US
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## U.S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature	<i>Ch. Q</i>	Date Considered	<i>1-21-07</i>
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Substitute for form 1449B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		<b>Complete if Known</b>	
		Application Number	10/743,307
		Filing Date	December 23, 2003
		First Named Inventor	ZHANG, Qi
		Art Unit	2493 2193
		Examiner Name	Not yet known - Chris, J.
Sheet	2	of	2
		Attorney Docket Number	P-6215-US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (where appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
Q	D	ANONYMOUS: "Rounding-mode independent implementation of float-to-integer truncation", Research Disclosure, April 2000, vol. 432, no. 58, Mason Publications, Hampshire, GB.	<input type="checkbox"/>
Q	E	INTEL CORPORATION: "Intel Architecture Optimization-Reference Manual"[Online], February 1999, pp.2-25, 2-27, 5-42. Retrieved from <a href="http://www.intel.co.jp/design/pentium/it/manuals/24512701.pdf">www.intel.co.jp/design/pentium/it/manuals/24512701.pdf</a>	<input type="checkbox"/>
Q	F	International Search Report dated March 30, 2006 for Application No. PCT/US2004/041849.	<input type="checkbox"/>
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Examiner Signature	Chris, J.	Date Considered	1-21-07
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<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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